

**FIG. 1**  
**(Prior Art)**

200

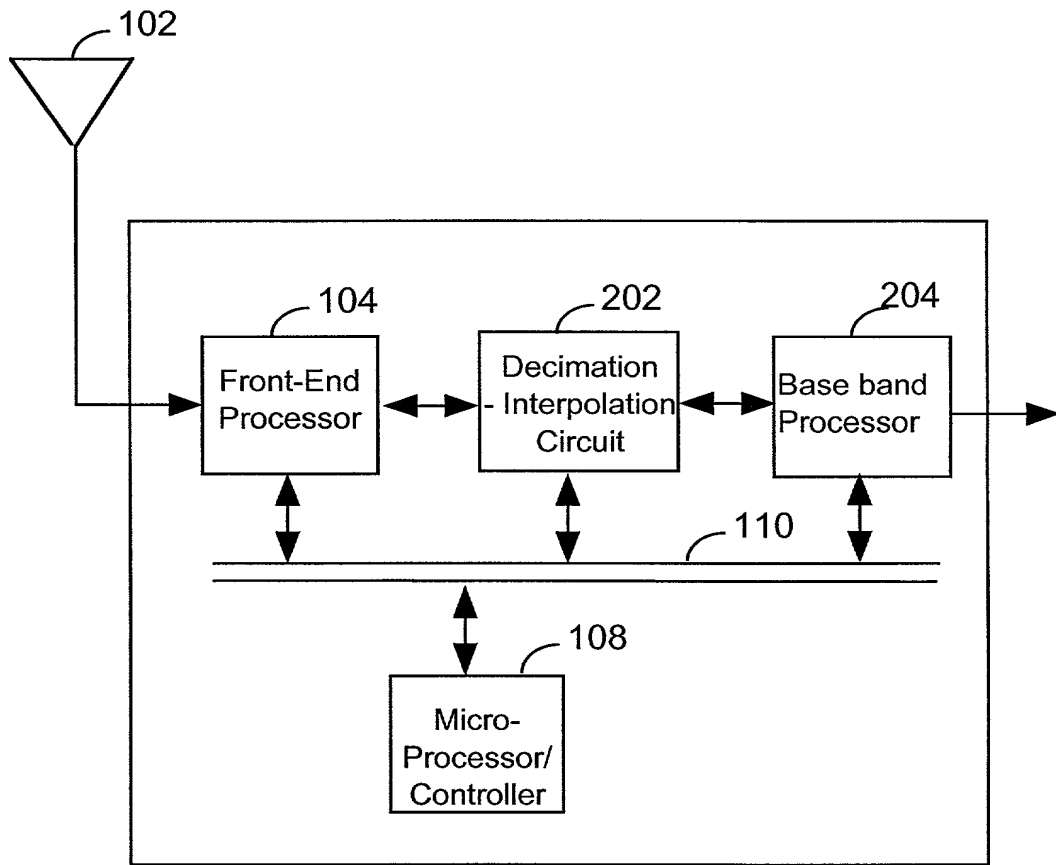
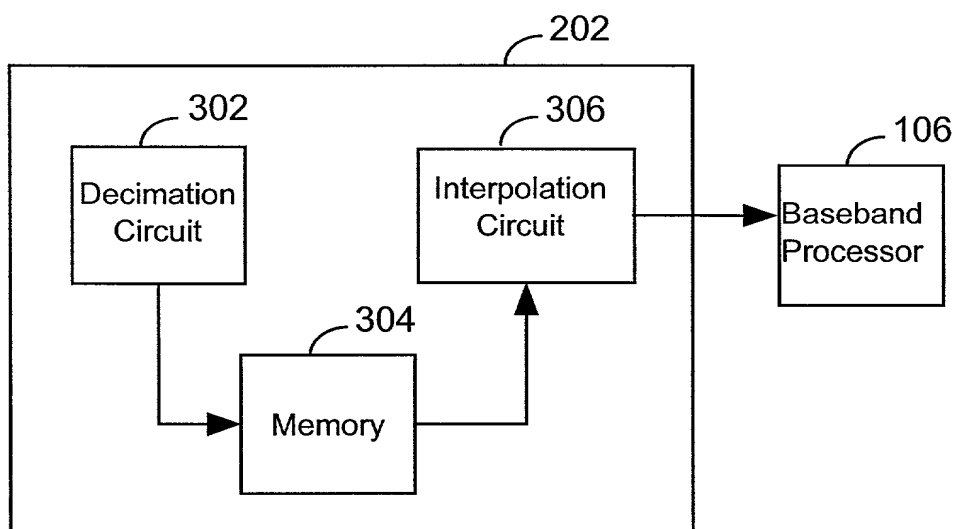
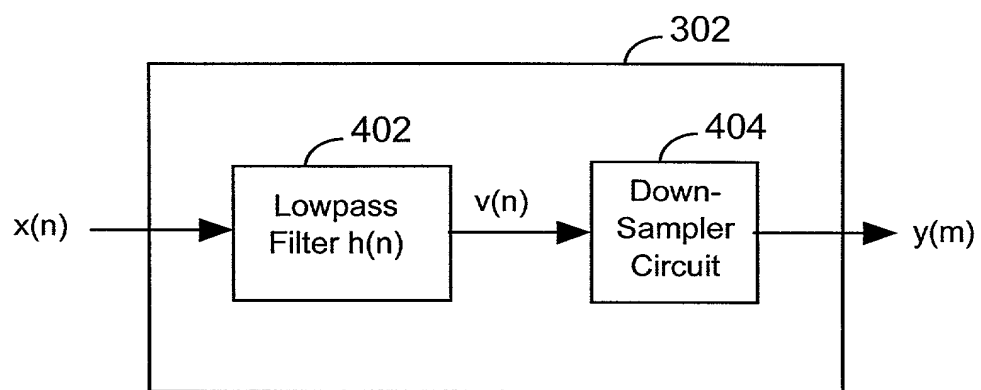


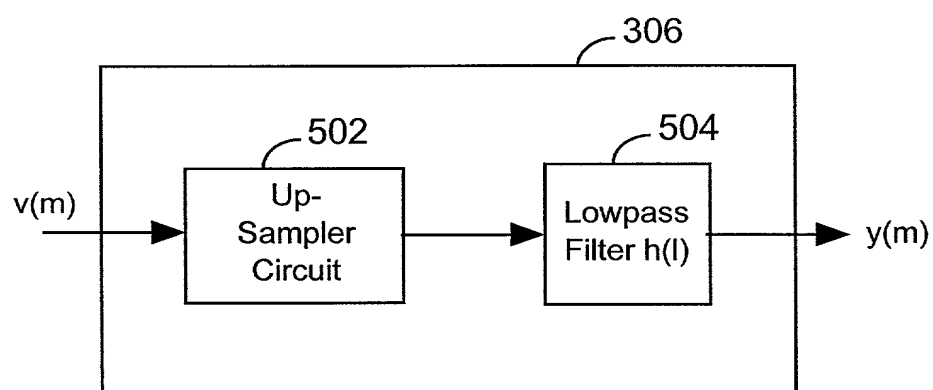
FIG. 2



**FIG. 3**



**FIG. 4**



**FIG. 5**

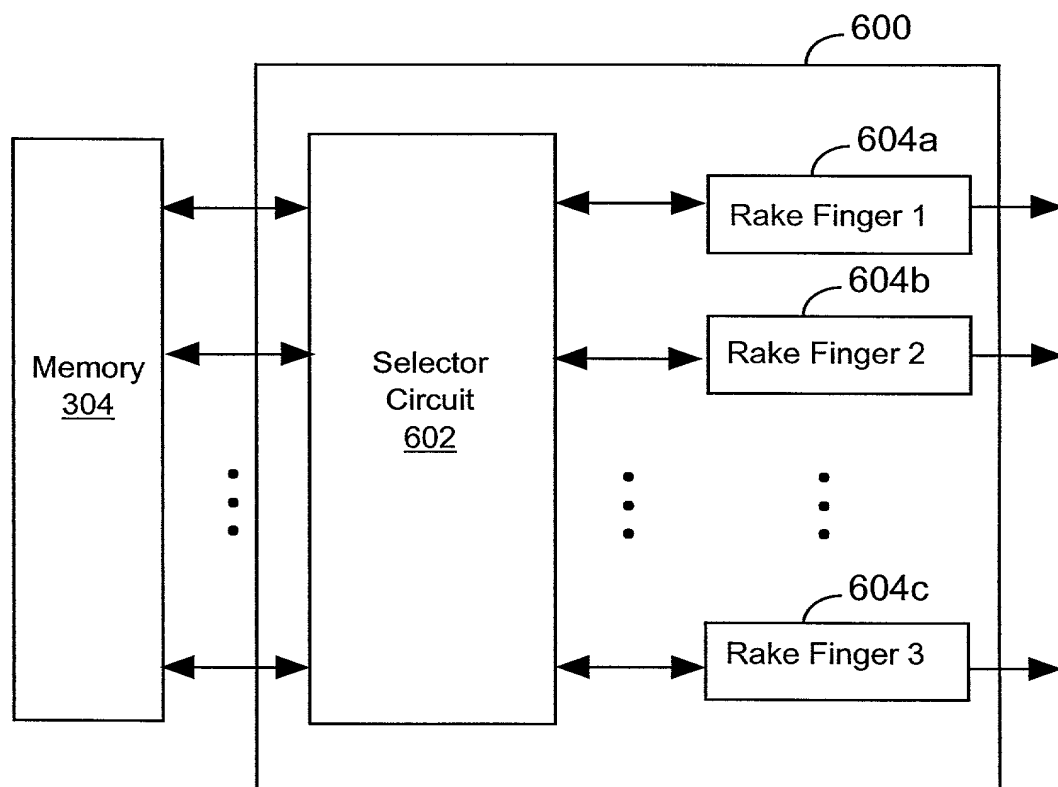


FIG. 6

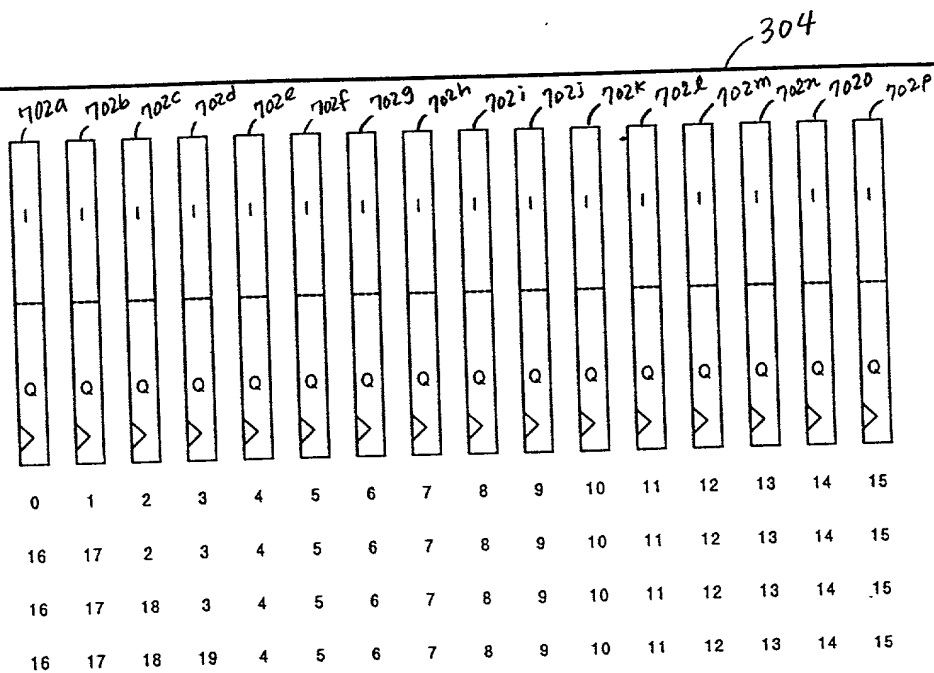


Figure 7

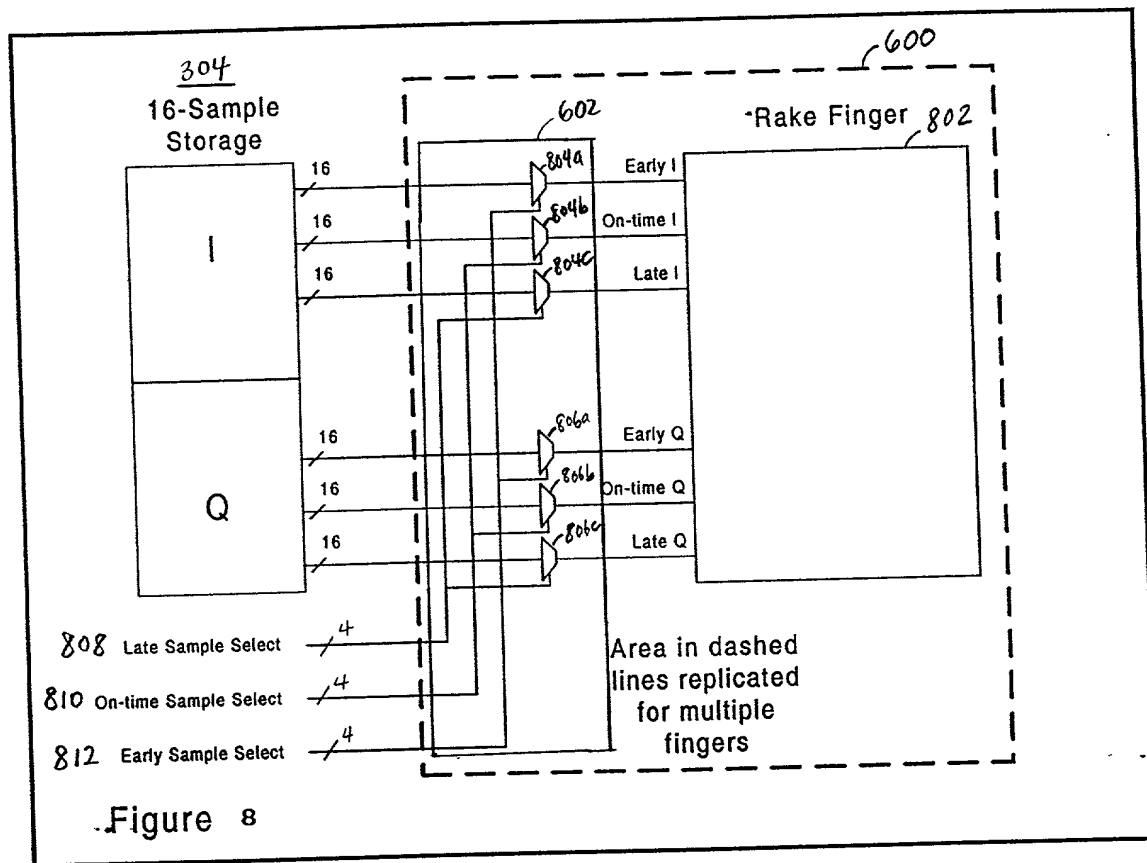
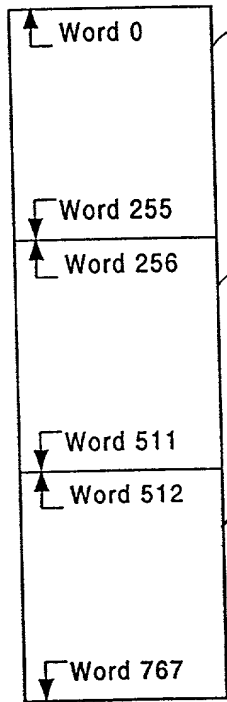
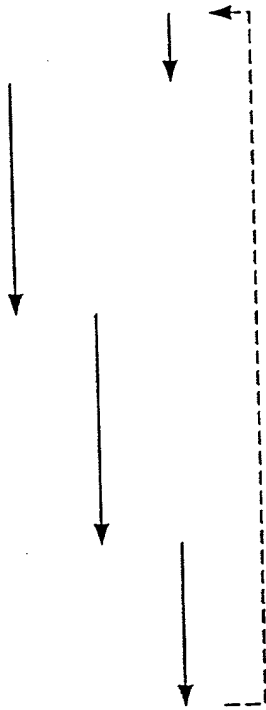


Figure 8

Pass1

Pass2

Pass3



902a

Block of 256 chips  
worth of samples

902b

Block of 256 chips  
worth of samples

902c

Block of 256 chips  
worth of samples

Pass1

Pass2

Pass3

Read

Write

Read

Read

Read

Write

Write

Read

Read

Figure 9

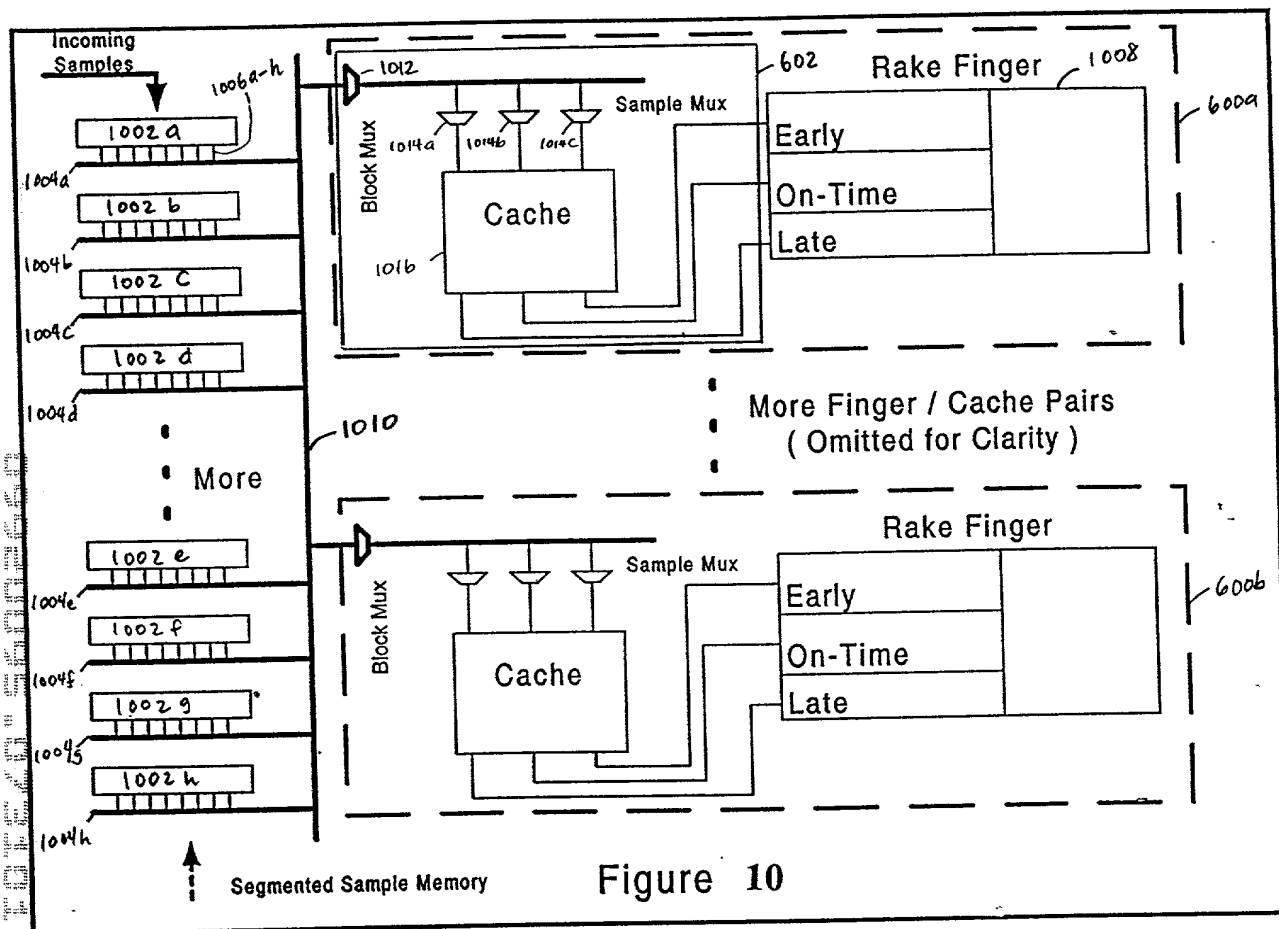
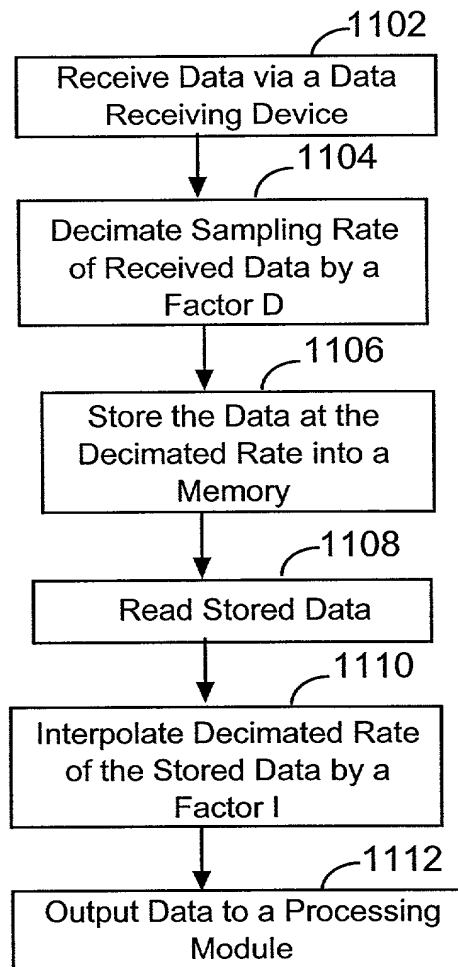


Figure 10



**FIG. 11**

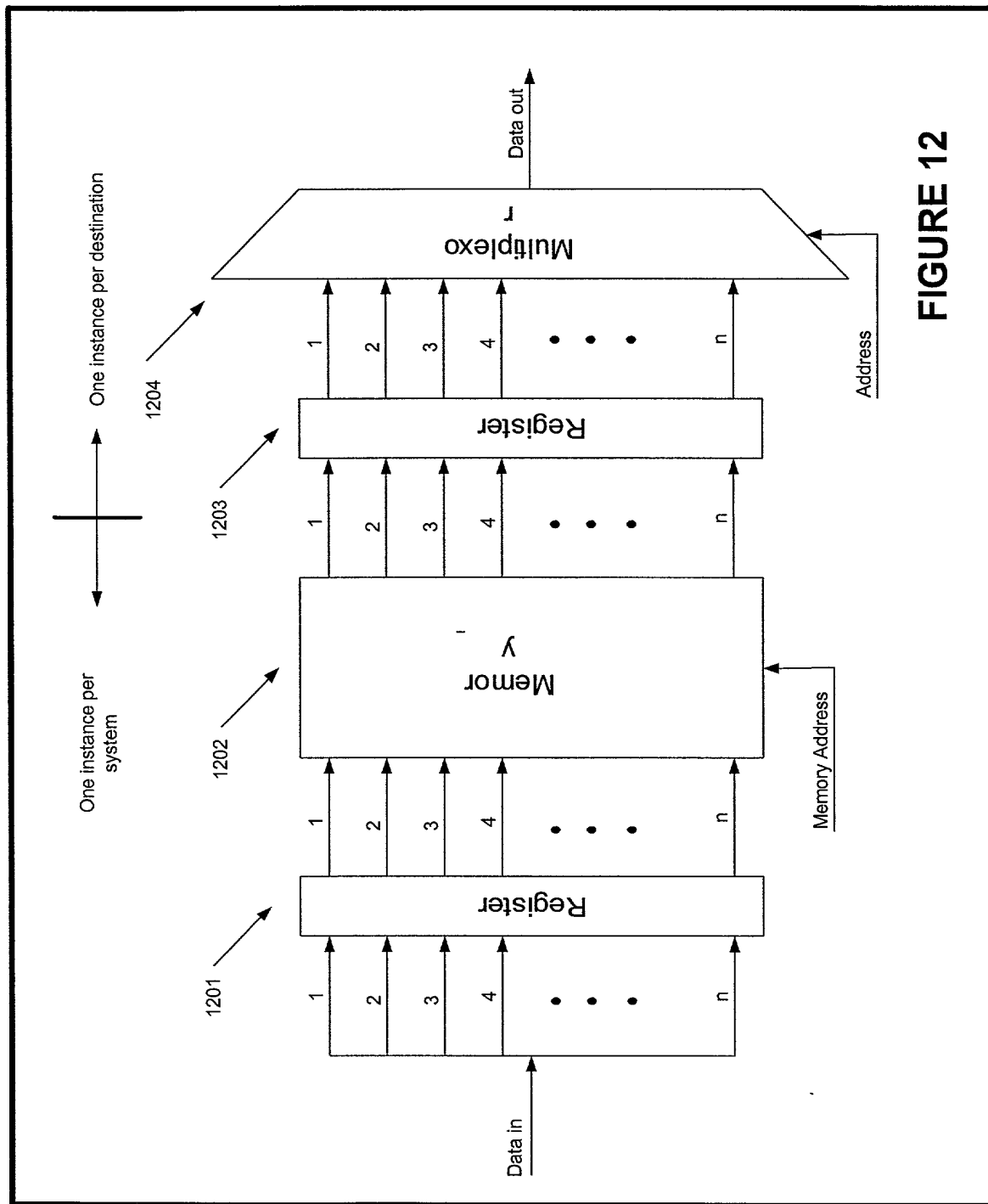


FIGURE 12

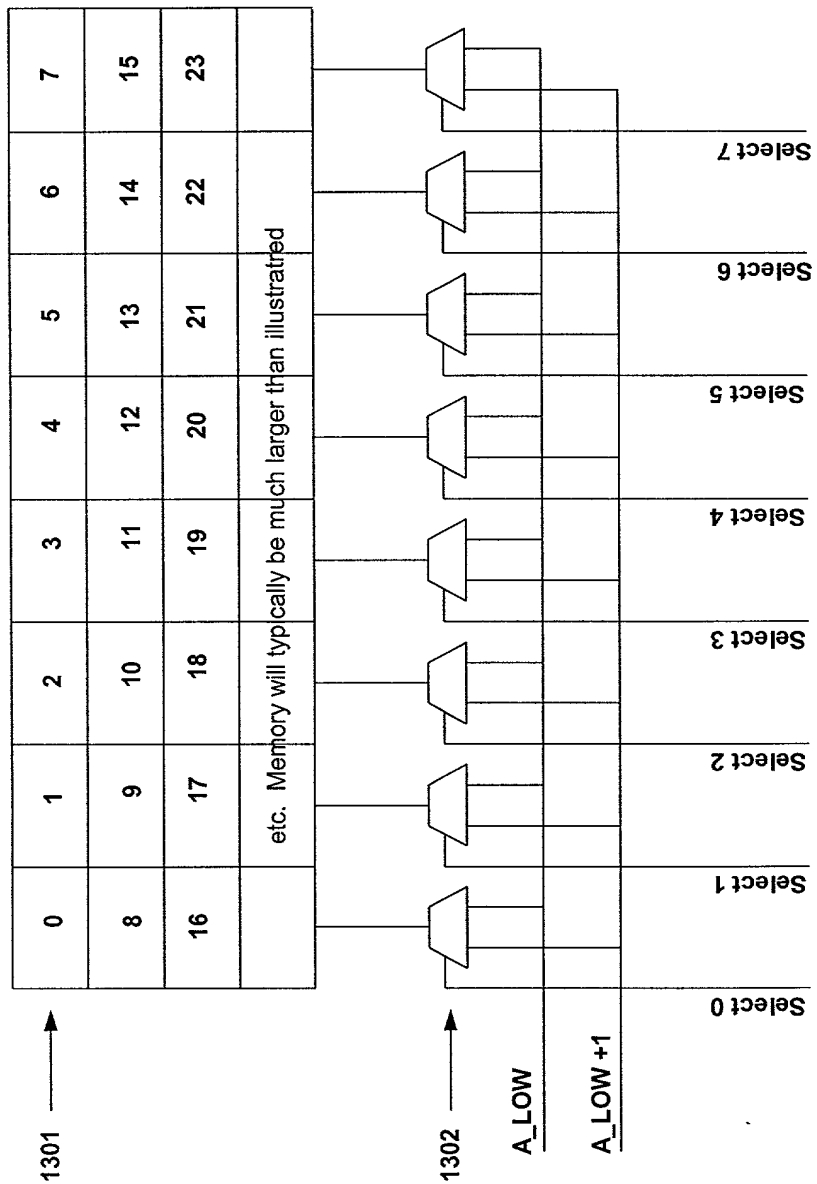


FIGURE 13